

1-862 U.S. PTO  
09/626146

07/36/00

257 710

Class Subclass

ISSUE CLASSIFICATION

PATENT NUMBER

# U.S. UTILITY Patent Application

SCANNED PD<sup>2</sup> QA AG

O.I.P.E. PATENT DATE

APPLICATION NO. 09/626146

CLASS 438

SUBCLASS 710

ART UNIT 2812

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INVENTOR PRIOR F

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APPLICANTS

TITLE

SUBSTRATE FOR SEMICONDUCTOR DEVICE, SEMICONDUCTOR CHIP MOUNTING SUBSTRATE, SEMICONDUCTOR DEVICE AND METHOD OF FABRICATION THEREOF, AND CIRCUIT BOARD, TOGETHER WITH ELECTRONIC EQUIPMENT

PTO-2040  
12/99

## ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
257	710	257	709	736	788		
INTERNATIONAL CLASSIFICATION							
H 01 L	23/10						
H 01 L	23/12						

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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	8	16	13		1
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____	<p>OKI NADAV 11/1/02</p> <p>(Assistant Examiner) (Date)</p>			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<p>TOM THOMAS</p> <p>SUPERVISORY PATENT EXAMINER</p> <p>TECHNOLOGY CENTER 2800</p> <p>T. Thomas 11-7-02</p> <p>(Primary Examiner) (Date)</p>			<p>ISSUE FEE</p> <p>Amount Due Date Paid</p>	
	<p>(Legal Instruments Examiner) (Date)</p>			<p>ISSUE BATCH NUMBER</p>	

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